## DIGITAL COMPUTER FUNDAMENTALS Multiple Choice Questions \& Answers

1. A NOT gate has $\qquad$ .
A. two inputs and one output.
B. one input and one output.
C. one input and two outputs.
D. two inputs and two outputs.

## ANSWER: B

2. An OR gate has $\qquad$ .
A. two inputs and one output.
B. one input and one output.
C. one input and two outputs.
D. one input and three outputs.

## ANSWER: A

3. The output of a logic gate can be one of two $\qquad$ .
A. inputs.
B. gates.
C. states.
D. outputs.

## ANSWER: A

4. A group of 4 cells in K-map is called $\qquad$ .
A. octet.
B. pair.
C. triple.
D. quad.

## ANSWER: C

5. A group of 8 cells in K-map is called $\qquad$ .
A. octet.
B. pair.
C. triple.
D. quad.

ANSWER: D
6. In a maxterm a variable appears $\qquad$ .
A. 1 in uncomplemented and 0 in complemented.
B. 0 in uncomplemented and 2 in complemented.
C. 2 in uncomplemented and 1 in complemented.
D. 0 in uncomplemented and 1 in complemented.

## ANSWER: D

7. How many inputs and outputs are used to construct a full adder?
A. 3 inputs and 2 outputs.
B. 2 inputs and 1 output.
C. 1 input and 1 output.
D. 1 input and 2 outputs.

## ANSWER: A

8. How many inputs and outputs are used to construct a full subtractor?
A. 2 inputs and 2 outputs.
B. 2 inputs and 1 output.
C. 1 input and 1 output.
D. 3 inputs and 2 outputs.

ANSWER: D
9. Which combinational circuit that performs subtraction involving three bits?

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A. Single subtractor.
B. Half subtractor.
C. Full subtractor.
D. Multi subtractor.

## ANSWER: C

10. A register is a group of $\qquad$ suitable for storing binary information.
A. counters.
B. adders.
C. flip flops.
D. subtractors.

ANSWER: C
11. An $n$-bit register has a group of $\qquad$ flip flops and is capable of storing any binary information containing $n$ bits.
A. $\mathrm{n}-1$.
B. $n(n-1)$.
C. n .
D. n 2 .

## ANSWER: C

12. A group of flipflops sensitive to pulse duration is called $\qquad$
A. registers.
B. latch.
C. multiplexer.
D. demultiplexer.

ANSWER: B

