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1. A single stage transistor amplifier contains and associated circuitry

Two transistors One transistor Three transistor None of the above **Ans : 2**

2. The phase difference between the output and input voltages of a CE amplifier is

3. It is generally desired that a transistor should have input impedance

Low Very low High Very high **Ans : 3**

4. When an a.c. signal is applied to an amplifier, ,the operating point moves along

c. load line c. load line both d.c. and a.c. load lines none of the above **Ans : 2**

5. If the collector supply is 10V, then collector cut off voltage under d.c. conditions is

20 V 5 V 2 V 10 V **Ans : 4**

6. In the zero signal conditions, a transistor sees load

c. c. both d.c. and a.c. none of the above **Ans : 1**

7. The input capacitor in an amplifier is the capacitor

Coupling Bypass Leakage None of the above **Ans : 1**

8. The point of intersection of d.c. and a.c. load lines is called

Saturation point Cut off point Operating point None of the above **Ans : 3**

9. The slope of a.c. load line is that of d.c. load line

The same as More than Less than None of the above **Ans : 2**

10. If a transistor amplifier draws 2mA when input voltage is 10 V, then its input impedance is

20 kΩ 2 kΩ 10 kΩ 5 kΩ **Ans : 4**